

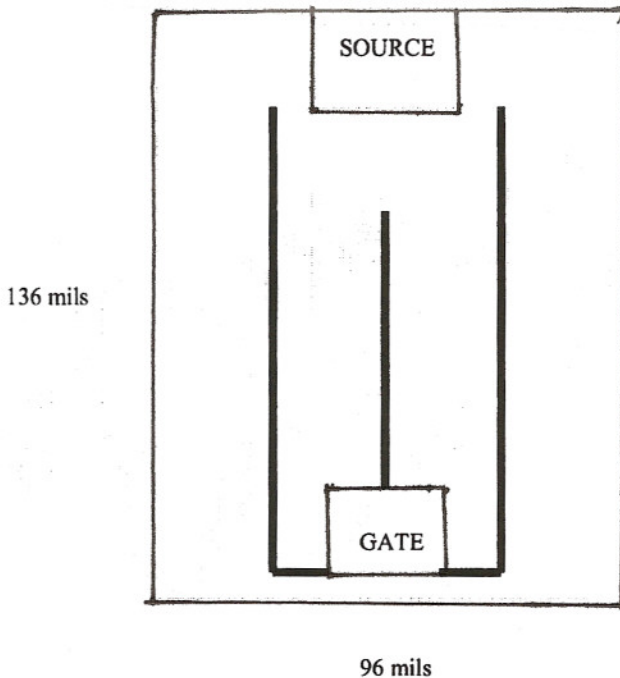


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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



CHIP BACK IS DRAIN

Topside Metal: Aluminum

Backside Metal: CrNiAg

Backside Potential: Drain

Bond Pad Size: .004" min.

Mask Ref:

MFG: IRI

THICKNESS: .016"

Part # IRFC320BV

APPROVED BY: J Kidd

DIE SIZE : .098" X .136"

DATE: 3/26/08